http://www.ijise.in

(IJISE) 2016, Vol. No. 3, Jan-Jun

INTEGRATING MODERN TECHNOLOGIES FOR WIRELESS CHARGER FOR MOBILE PHONE SYSTEMS

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ABSTRACT

No more worrying about forgetting to charge the battery. It is the motivation of this paper to converse the first step toward realizing this goal. A system will be offered using existing antenna and RFID technology to charge a mobile phone battery without using wires. In this first step, it will use a standard phones, and combine the charging technology into a commercially available base station. The base station will contain an antenna tuned to 915MHz and a charge pump. In this paper to discuss about the advantages and disadvantages of such a system, and hopefully pave the way for a system incorporated into the phone for charging without the use of a base station.

Keywords: RFID, Smart Antenna, Wireless charger, Cellular Technology and RF system.

1. INTRODUCTION

Through the years, technology has permitted the mobile phone to shrink not only the size of the ICs, but also the batteries. However, still have one of the original problems, it must plug the phone into the wall in order to recharge the battery. The objective of this paper is to determine if it is possible to capture sufficient power in a cellular phone in order to charge the battery. It was being bombarded with energy waves every second of the day. Radio and television towers, satellites orbiting earth, and even the cellular phone antennas are constantly transmitting energy. If there was a way it could harvest the energy that is being transmitted and use it as a source of power. If it could be possible to gather the energy and store it could possibly use it to power other circuits. In the case of the mobile phone, this power could be used to recharge a battery that is frequently being depleted. The potential exists for mobile phones, and even more complicated devices pocket organizers, person digital assistants (PDAs), and even notebook computers to become completely wireless [2].

(IJISE) 2016, Vol. No. 3, Jan-Jun

e-ISSN: 2454-6402, p-ISSN: 2454-812X

2. SMART TECHNOLOGIES

RF energy is transmitted to the circuit and the charge pump stores the energy in a large capacitor [1]. When the amount of charge is large sufficient, the LED uses the stored energy to light for a moment. This is referred as charge-and-fire system. This kind of technology is very useful in Radio Frequency Identification applications. The method systems work is that when a chip passes through a scanner device, power is sent to the chip from the scanner. In previous systems, the frequency or amplitude of this signal was modulated by the chip and sent back. This method is named backscatter. The RFID system is suitable for batteries because they are small. Hence this method can be used for this system along with a good antenna and a charge pump. A charge pump should be able to handle the powering of the circuits and never will need to be serviced. Because the circuits are small, the requirement of power is very minimum.

Transmitter Section:

The most basic transmitter setup consists of a piece of equipment that generates a signal whose output signal is then fed into an amplifier that is finally output through a radiating antenna the air interface. The RF source is a circuit that output a signal at a user-specified frequency and voltage. The choice of frequencies of the signal generator resides in the radio frequency band, 3 mega-hertz (MHz) to 3 giga-hertz (GHz). The frequency of 915MHz can be selected for this paper as it falls in one of the Industrial-Scientific-Medical (ISM) RF bands made available by the Federal Communications Commission for low power system, short distance experimentation. This frequency is chosen frequently for simplicity in using the available equipment [3]. It is not used for mass communication or anything else on a main scale, and therefore is not going to be interfered with, or interfere with other devices at low power system levels.

The Charge Pump:

At this point, it is essential to describe what exactly a charge pump is, and how it works. A charge pump is a circuit when given an input in AC is able to output a DC voltage typically larger than a simple rectifier would generate. It can be thought of as a AC to DC converter that both rectifies the AC signal and increases the DC level. It is the foundation of power converters such as the ones that are used for many electronic devices today. The easiest design that can be used is a peak detector or half wave peak rectifier. This circuit requires only a capacitor and a diode to function. The peak detector circuit diagram is shown in Figure 1. The description of how this circuit works is quite easy.

The AC wave has two halves, one positive and one negative cycle. On the positive half cycle, the diode turns on and current flows, charging the capacitor. On the negative half of the wave, the diode is off such that no current is flowing in either direction. Now, the capacitor has voltage built up which is equal to the peak of the AC signal, hence the name peak detector [4]. Without

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the load on the circuit, the voltage would hold indefinitely on the capacitor and look like a DC signal, assuming ideal components. With the load, however, the output voltage decreases during the negative cycle of the AC input.



Figure 1: Peak Detector Circuit

This is due to the RC time constant. The voltage decreases in relation to the inverse of the resistance of the load, R, multiplied by the capacitance C. This type of circuit generates a lot of ripple, or noise, on the output DC of the signal. With more circuitry, that ripple can be reduced. Whereas the earlier circuit only arrests the positive cycle of the signal, here both halves of the input are captured in the capacitor. In the positive half of the cycle, diode D1 is on, diode D2 is off and charge is stored on the capacitor. But, during the negative half, the diodes are reversed, D2 is on and D1 is off [9-16].

The capacitor doesn't discharge nearly as much as in the earlier circuit, so the output has much less noise. It generates a cleaner DC signal than the half-wave rectifier, but the circuit itself is much more complicated with the outline of a transformer. This basically rules this methodology out for this paper because of the space needed to implement it.

The other methodologies for charge pumps are more difficult and all include transformers, like the full-wave rectifier etc. The charge pump circuit is made of stages of voltage doublers. This type of circuit is referred as voltage doubler because in theory, the voltage that is received on the output is twice that at the input. The RF wave is rectified by diode D2 and capacitor C2 in the positive half of the cycle, and then by diode D1 and capacitor C1 in the negative cycle. But, during the positive half-cycle, the voltage stored on C1 from the negative half-cycle is transferred to C2. Thus, the voltage on C2 is roughly two times the peak voltage of the RF source

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minus the turn-on voltage of the diode, hence the name voltage doublers [5], [6]. The most motivating feature of this circuit is that by connecting these stages in series, it can essentially stack them, like stacking batteries to get more voltage at the output. This is where the other stages come in. If a second stage is added on top of the first, the only wave that the second stage seen is the noise of the first stage. This noise is then doubled and added to the DC of the first stage. Therefore, the more stages that are added, theoretically, more voltage will come from the system regardless of the input [17-27]. Each independent stages, with its dedicated voltage doubler circuit, can be seen as a battery with open circuit output voltage V_0 and internal resistance R_0

Experimental Test Setup

The testing setup for this project is shown in Figure 2. As it can see that, the output of the charge pump circuit is input directly into the battery. This is one of the two ways to charge the battery. This is to power the phone through its DC input circuitry, and let it charge the battery. It was noted that by the implementation of this system the battery can be charged at a rate of 2mV per second. This is an average result, obtained by comparing with conventional type chargers.



The Antenna Section

The most straightforward option for the receiving antenna is to use an existing antenna that can be attained commercially. It was found that a quarter-wave whip antenna can be used for the testing purposes. This antenna is analogous to that used on car radios. It is named as quarter-wave antenna because it is designed so that its length is approximately one quarter of the wavelength of the signal. This means that for a 915MHz signal, with a wavelength equal 32cm, a quarter-wave antenna would have an 8cm length. A picture of the quarter-wave whip antenna is shown in Figure 3.

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Figure: 3 Quarter-wave Whip Antenna

The large copper plate is the ground plane. The antenna is attached to the copper, with an SMA connector on the underside of the ground plane. This type of connector uses a simple screw mechanism allowing for easy connectivity with other circuits and test equipment. The cord is connected on the other side to the BNC connector of the board. As it can see, this ground plane is rather large, too large to be used inside the stand for a mobile phone. It covers almost 50% more area than the stands that were selected for this purpose. Other types of antennas to consider are patches, micro strips, dipoles, and monopoles.

3. CONCLUSION

Nevertheless, like most people, probably it does not seem good in being tethered to the wall. Envision a system where the cellular phone battery is continuously charged. No more perturbing about forgetting to charge the battery. It is the focus of this paper to deliberate the first step towards realizing this objective. Here, a first step towards an objective that would have thoughtful ramifications on the cellular phone industry and the portable electronic device industry as a whole has been envisaged. If this goal is realized it would be sure that wireless battery charging system will be a milestone in the account of wireless technology.

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e-ISSN: 2454-6402, p-ISSN: 2454-812X

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